

**REMARKS/ARGUMENTS**

Claims 1-14, 16-23 and 25-27 are currently pending in this application.

**Claim Rejections - 35 USC § 103**

Claims 10-14, 16-23 and 25-27 are rejected under 35 U.S.C. 103(a) as being unpatentable by U.S. Patent No. 6,721,370 (Kurihara) in view of U.S. Patent No. 5,933,112 (Hiramatsu et al.) and U.S. Patent No. 6,308,057 (Hayashi). Furthermore, claims 10-14, 16-23 and 25-27 are rejected under 35 U.S.C. 103(a) as being unpatentable by U.S. Patent No. 6,721,370 (Kurihara) in view of U.S. Patent No. 6,340,883 (Nara et al.) and U.S. Patent No. 6,587,513 (Ichihara).

Regarding claims 10 and 19, Kurihara discloses a phase correction circuit comprising a modem 22/12, a phase correction units 24/14, an automatic gain control (AGC) circuit 21/11/15 and a baseband processor 31.

A modem is a well known device that is configured to modulate signals for transmission and demodulate signals that are received. The Examiner asserts that the baseband processor 31 disclosed by Kurihara (see Figure 3) is a modem. The Applicants strongly disagree. Kurihara discloses a modulator 22 and a demodulator 12 (i.e., a modem). Certainly, Kurihara does not disclose two modems in series. The baseband processor 31 outputs a transmission signal 53, a gain signal 55 and a timing signal 44. The baseband processor 31 does not modulate and demodulate signals, and thus the baseband processor 31 is not a modem. The baseband processor 31 encodes audio signals input from a microphone 33 (see column 4, lines 25-37), but does not modulate the audio signals. The modulator 22 modulates signals output by the baseband processor 31 after they are phase corrected and converted from digital signals to analog signals by a digital-to-analog (D/A) converter 23. The demodulator 12 demodulates signals input to the baseband

processor 31 after the signals are converted from analog signals to digital signals by an analog-to-digital (A/D) converter 13 (see Figure 3).

Kurihara discloses a modem 22/12 coupled to an AGC circuit 22/12. However, Kurihara fails to teach or suggest that the modem 22/12 outputs a gain control signal. Furthermore, the Applicants submit that none of Hiramatsu, Hayashi, Nara and Ichihara teach this feature.

Claims 11-14, 16-18, 20-23 and 25-27 are dependent upon claims 10 and 19, and the Applicants believe that these claims are allowable over the cited references of record for the same reasons provided above.

Based on the arguments presented above, the withdrawal of the 35 U.S.C. 103(a) rejection of claims 10-14, 16-23, 25-27 is respectfully requested.

### **Conclusion**

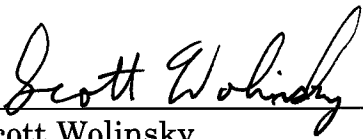
If the Examiner believes that any additional minor formal matters need to be addressed in order to place this application in condition for allowance, or that a telephonic interview will help to materially advance the prosecution of this application, the Examiner is invited to contact the undersigned by telephone at the Examiner's convenience.

**Applicant:** Demir et al.  
**Application No.:** 10/736,432

In view of the foregoing remarks, the Applicants respectfully submit that the present application is in condition for allowance and a notice to that effect is respectfully requested.

Respectfully submitted,

Demir et al.

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